ECE 3223
Homework 9

Fall 1997 Dr. Havlicek

Work the following problems from the course text:

Chapter Three:

3.1; If only registers are involved, then give the high-level RTL for the instruction. If memory is involved, then be sure to specify the memory segment and the offset address.

3.7

3.9

3.11

3.13

3.18

3.21

3.25; part (c) ONLY.

DUE: 11/7/97.